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Attorney Docket: 040021-0305238
Client Reference: OPP 030839 US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT APPLICATION of:

Confirmation Number: 9322

PARK

Application No.: 10/626,549

Group Art Unit: 2814

Filed: July 25, 2003

Examiner: H. PHAM

Title: METHOD FOR MANUFACTURING MOS TRANSISTOR THAT PREVENTS CONTACT SPIKING AND SEMICONDUCTOR DEVICE EMPLOYING A MOS TRANSISTOR MADE USING THE SAME

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL OF FORMAL DRAWINGS

Attached is a formal drawing for this application. One (1) sheet of Figures 1A and 1B.

Date: February 3, 2005
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